can testing 21-08-19

1. Can network tested for 4 nodes, 2 stm and 2 arduinos
2. Stm32 clock at 72mhz and network at 500kbps ; messages received output is random.
3. Stm32 clock at 72mhz and network at 1mbps ; messages received output is random.
4. Message id 1526 is missing, second stm is not reading msg 26.

Msg 25 was removed, msg 26 was sent, second stm is reading the message but is not sending msg 27,28,29.

1. Implemented can read for second stm, so that stm2 sends msg 32 33 only after reading 31 msg of stm1. 5ms delay was not included in the if structure.

Issue: msg 32, 33 were not coming and even though stm2 is receiving 31 messages. Also 30,31 is coming once.

1. Stm clocks switched to 64mhz at 1mbps.

Output : 20, 21, 22, 23, 24, 26, 30, 31, 30, 31,

This means Stm2 msgs are not being read.

Also mcp2515 is giving buffer overflow error(A3) in 2b register.

1. Adding delay of either 5ms or 500us is giving

Output: only ecu messages, 20,21,22.

1. Stm at 32mhz at 1mbps

Output : only ecu messages.

1. Stm at 40mhz at 1mbps

Output: 20, 21, 22, 23, 24, 26, 30, 31, 30, 31,

1. Delay was added to stm2 before sending can msgs in both if structure

Output: no change

1. Sepreate can msg was sent from stm2, at 50hz is being read by myrio,
2. Both stms were powered with usb interface rather than pads,

Output improved greatly.

Output : 30,31,32,33, 20, 21, 22, 23, 24, 26,27,28,29, 30, 31,32,33, 30, 31,32,33

The order is correct but it is not starting at 20, error at myrio side

1. Msg 25 was added again, but msg 26 was removed as the stm32 buffer is only 3.

Adding a delay of 250us solves this issue.

1. Both stms set at 72mhz at 1mbps again.

Output: wrong output

1. Final setup: both stms at 40mhz at 1mpbs both powered via pads with buck converter at 3.3v from a 12v car battery.